## PRINTER RUSH

(PTO ASSISTANCE)

		201	request		
Application:	/078056 PAP	Examiner:	Lee	GAU:	2881 7/11/05
From: _		Location:	IDC FMF FDC	Date:	
Tracking #: <u>06075487</u> Week Date: <u>217/05</u>					
	DC CODE  1449  IDS  CLM  IIFW  SRFW  DRW  OATH  312  SPĒC	DOC DATE	MISCELL Continuing Foreign Price Document I Fees Other	Data ority	
[RUSH] MESSAGE: Lenymbered clams 31, 32, 35 (original claims 37, 37, 36 depend on renumbered claim 37 (original claim 36).  Thank you.  [XRUSH] RESPONSE:					
Tu de pond on ren umberel claum 34					
INITIALS					

NOTE: This form will be included as part of the official USPTO record, with the Response document coded as XRUSH.

**REV 10/04** 

- (Previously Presented) The system of claim 36, wherein electric charges drawn across the semiconductor layer is greater near the first surface of the semiconductor layer adjacent to the charge-collection layer relative to the second surface.
  - 32 24. (Previously Presented) The digital radiography system of claim 26, wherein the flat panel imager is a TFT-based imager.
  - 33 35. (Previously Presented) The digital radiography system of claim 36, wherein the flat panel imager is a CCD-based imager.
  - 34 (Previously Presented) A digital radiography system, comprising: an x-ray source to transmit x-rays;
    - a flat panel imager to receive the x-rays and to produce a digitized image, comprising:
      - a semiconductor layer disposed above a charge-collection layer;
        a bias electrode layer disposed above the semiconductor layer, the
        bias electrode to generate an electric field within the semiconductor layer;
        and
    - a casing that holds the flat panel imager together, wherein the casing forms an aperture window to receive the x-rays; and a display system connected to the flat panel imager, the display system to display the digitized image, wherein the semiconductor layer has a first surface adjacent to the charge-collection layer and a second surface adjacent to the bias electrode, and wherein the flat panel imager is configured such that x-rays traverse the charge-collection layer before propagating through the semiconductor layer.

-11-